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Inventor

ERDOGAN, et al

Group

2816

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(Use Several Sheets if Necessary)

U.S. Patent Documents

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Examiner Initial		Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication				
Mar	Н	Behbahani, Farbod, et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection", IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pg. 873.				
	I	Hornak, Thomas, et al., "An Image-Rejecting Mixer and Vector Filter with 55-dB Image Rejection over Process, Temperature, and Transistor Mismatch, IEEE Journal of Solid-State Circuits, Vol 36, No. 1, January 2001.				
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Examiner		Date Considered 1/5/05				

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.